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data from Table 1.1 (feature size vs.

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Deal and Griffin. c)  $x = (5 \times 10^{20} \text{ cm}^{-3})^{-1/3}$   
 $= 1.3 \times 10^{-7} \text{ cm} = 0.0013 \mu\text{m}$   
 $= 1.3 \text{ nm}$ . 1.3. Consider a piece of pure  
silicon 100  $\mu\text{m}$  long with a cross-  
sectional area of 1  $\mu\text{m}^2$ .

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P P+ P N well P-P+ After applying  
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the NMOS transistors. A boron implant then forms the PMOS source and drain regions.

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## **EE/MSE 528 Physics and Modeling of VLSI Fabrication**

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below illustrates a simple bipolar transistor fabricated as part of a silicon IC. (See also Figure 1-32.)

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