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This invaluable textbook covers the theory and circuit design techniques to

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implement CMOS (Complementary Metal-Oxide Semiconductor) class-D audio amplifiers integrated circuits. The first part of the book introduces the motivation and fundamentals of audio amplification. The loudspeaker's operation and main audio performance metrics explains the limitations in the amplification process.

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receiver design. CMOS's raw performance is not as good as SiGe or GaAs. But most of the baseband (BB) ICs are implemented with CMOS. Thus, it gives the advantage to CMOS in applications where a single chip that combines RF and BB IC is desired.

Design considerations for

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This invaluable textbook covers the theory and circuit design techniques to implement CMOS (Complementary Metal-Oxide Semiconductor) class-D audio amplifiers integrated circuits. The first part of the book introduces the motivation and fundamentals of audio amplification. The loudspeaker's

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In this thesis, the design of a fully integrated RF CMOS phase-locked loop is explored. The goal of this research is to provide solutions for the problems associated with the VCO and the frequency divider in the RF CMOS phase-

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locked loop. There are five important contributions in this research. Firstly, a method for

Novel Techniques for Fully Integrated RF CMOS Phase-Locked

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CMOS integrated circuit design for wireless power transfer intends to report

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the state-of-the-art analog and power management IC design techniques for various wireless power transfer (WPT) systems. To propose elaborate power management solutions, the circuit designers are required to have in-depth understanding on the

Yan~Lu Wing-Hung~Ki CMOS

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BiCMOS and CMOS 5-V technologies are presented.

Design techniques for high-speed, high-resolution comparators

2 Amplifier design + Show details-Hide details p. 27 -73 (47) A 141-GHz amplifier was designed for the 40-nm CMOS process. The core was kept

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reasonably compact $201 \times 284 \mu\text{m}^2$ in spite of the five-stage differential amplifier by using a layout avoiding the coupling effect of adjacent shunt stub lines. The peak gain and 3-dB bandwidth were 20dB at 148 and 22 GHz, respectively.

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design can be divided into the broad categories of ...

Integrated circuit design - Wikipedia

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Abstract. In this paper, a review and analysis of different design techniques for (ultra) low-voltage integrated circuits (IC) are performed. This analysis shows that the most suitable design methods for low-voltage analog IC design in a standard CMOS process include techniques using bulk-driven MOS transistors, dynamic threshold MOS

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transistors and MOS transistors operating in weak or...

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The 65nm Bulk CMOS prototype discussed in Chapter 4 also utilizes only standard MOS capacitors to regulate the output voltage at $\sim 1V$ from a $\sim 2.9V$ - $4V$ input. ... Thesis A Le, Hanh-Phuc
Design Techniques for Fully Integrated Switched-Capacitor Voltage

